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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Application No.: 09/965,275  
Filed: September 27, 2001

Inventor(s):  
Kim et al.

Title: INTEGRATED CIRCUIT  
CHIP TEST ADAPTER

§ Examiner: Nguyen, T.  
§ Group/Art Unit: 2829  
§ Atty. Dkt. No: 5681-49500

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Erik A. Heter  
Printed Name  
Signature 7/22/03 Date

**AMENDMENT; RESPONSE TO OFFICE ACTION OF**  
**April 23, 2003**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of April 23, 2003, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

**REMARKS**

Claims 1-24 are currently pending in the application. Claims were withdrawn.

**Elections/Restrictions:**

The species of claims 1-24 were elected without traverse for the restriction requirement discussed in the Office Action, in a telephone conversation between the Examiner and B. Noel Kivlin on April 16, 2003.

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